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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Wolrich et al.

Art Unit :

Examiner:

Serial No.:

: February 17, 2004

Filed Title

: Memory Mapping In a Multi-Engine Processor

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Under 35 USC §120, this application relies on the earlier filing date of application serial number 010736/0367, filed on April 3, 2000. The following references were submitted to and/or cited by the Office in the prior application and, therefore, are not provided in this application:

This statement is being filed with the application. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

2/17/04 Date:

> Arthur Ortega Reg. No. 53,422

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by A	closure Statement pplicant	Applicant Wolrich et al.	
(Use several sheets if necessary)		Filing Date	Group Art Unit
(37 CFR &1 98(b))			

			U.S. Pate	ent Documents		•	
Examiner Initial	Desig.	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,717,898	02/1998	Kagan et al.			
	AB	5,142,683	08/1992	Burkhardt, Jr. et al.			
	AC	5,404,464	04/1995	Bennett			
	AD	5,613,071	03/1997	Rankin et al.			
	AE	5,835,755	11/1998	Stellwagen, Jr.			
	AF						
	AG						
	AH						
	AI					-	
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner	Desig.	Document	Publication	Country or		Translation		lation
Initial	ID	Number	Date	Patent Office	Class	Subclass	Yes	No
	AL							
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	Other D	ocuments (include Author, Title, Date, and Place of Publication)
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